



RECEIVED

Applicant:

Sudhir K. Madan

Art Unit:

2818

MAR 0 1 2004

Serial No.:

09/921,394

Examiner:

TBD

OFFICE OF PETITIONS

Filing Date:

08/02/2001

Docket No.:

TI-30841

Conf. No.:

6004

Title: MEMORY WITH 6T SMALL ASPECT RATIO CELLS HAVING METAL_1

ELEMENTS PHYSICALLY CONNECTED TO METAL_0 ELEMENTS

Petition to Revive Abandoned Application under 37 C.F.R. §1.137(a) or in the alternative under 37 C.F.R. §1.137(b)

February 17, 2004

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(A)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as first Class Mail in an envelope addressed to Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450.

Marianna Smith

Dear Sir:

Applicant hereby petitions for revival of the above-referenced patent application. The above-reference patent application was unintentionally abandoned when applicants failed to respond to an Office Action dated 09/25/2001. The application was unintentionally abandoned due to the fact that the Office Action was stapled behind the Filing Receipt and not noticed until a review of the file after the Notice of Abandonment was received. As our docketing clerk did not notice it, the Office Action dated 9/25/2001 was not entered into the docketing system.

I first became aware of the Office Action when the Notice of Abandonment was received on February 2, 2004. A response to the Office Action is enclosed herewith.

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Applicant requests revival of the application under 37 CFR 1.137(b). The abandonment was unintentional. The Commissioner is hereby authorized to charge the fee under 37 CFR 1.17(I) for reviving the application and any other necessary fees to Deposit Account No. 20-0668.

Texas Instruments Incorporated P.O. Box 655474 M/S 3999 Dallas, Texas 75265 (214) 532-9348

Fax: (972) 917-4418

Respectfully submitted,

Sacqueline J. Garner Reg. No. 36,144



UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NUMBER

FILING OR 371(C) DATE

FIRST NAMED APPLICANT

ATTY. DOCKET NO./TITLE

09/921,394

08/02/2001

Sudhir K. Madan

TI-30841

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MAR 0 1 2004

23494 TEXAS INSTRUMENTS INCORPORATED P O BOX 655474. M/S 3999 DALLAS, TX 75265

OFFICE OF PETITIONS

CONFIRMATION NO. 6004 ABANDONMENT/TERMINATION LETTER

OC000000011796270

Date Mailed: 01/30/2004

NOTICE OF ABANDONMENT UNDER 37 CFR 1.53 (f) OR (g)

The above-identified application is abandoned for failure to timely or properly reply to the Notice to File Missing Parts (Notice) mailed on 09/25/2001.

No reply was received.

A petition to the Commissioner under 37 CFR 1.137 may be filed requesting that the application be revived.

Under 37 CFR 1.137(a), a petition requesting the application be revived on the grounds of UNAVOIDABLE DELAY must be filed promptly after the applicant becomes aware of the abandonment and such petition must be accompanied by: (1) an adequate showing of the cause of unavoidable delay; (2) the required reply to the aboveidentified Notice; (3) the petition fee set forth in 37 CFR 1.17(I); and (4) a terminal disclaimer if required by 37 CFR 1.137(d).

Under 37 CFR 1.137(b), a petition requesting the application be revived on the grounds of UNINTENTIONAL DELAY must be filed promptly after applicant becomes aware of the abandonment and such petition must be accompanied by: (1) a statement that the entire delay was unintentional; (2) the required reply to the aboveidentified Notice; (3) the petition fee set forth in 37 CFR 1.17(m); and (4) a terminal disclaimer if required by 37 CFR 1.137(d).

Any questions concerning petitions to revive should be directed to the "Office of Petitions" at (703) 305-9282 Petitions should be mailed to: Mail Stop Petitions, Commissioner for Patents, P.O. Box 1450, Alexandria VA 22313-1450.

A copy of this notice MUST be returned with the reply.

Center

Initial Patent Examination Division (703) 308-1202

PART 2 - COPY TO BE RETURNED WITH RESPONSE

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Sudhir K. Madan

Art Unit:

2818

RECEIVED

Serial No.:

09/921,394

Examiner:

TBD

MAR 0 1 2004

Filing Date:

08/02/2001

Docket No.:

TI-30 OFFICE OF PETITIONS

Customer No.: 23494

Conf. No.:

6004

Title: MEMORY WITH 6T SMALL ASPECT RATIO CELLS HAVING METAL 1

ELEMENTS PHYSICALLY CONNECTED TO METAL 0 ELEMENTS

RESPONSE TO NOTICE TO FILE CORRECTED APPLICATION PAPERS

Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. •1.8(a) I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner

for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Marianna Smith

Attn:

Customer Service Center

Initial Patent Examination Division

Dear Sir:

In response to the Notice to File Corrected Application Papers dated 09/25/2001, submitted herewith is an Abstract in compliance with 37CFR1.72(b) as required. Please amend the application to replace the original Abstract with the attached Abstract.

Respectfully submitted,

Texas Instruments Incorporated P. O. Box 655474, MS 3999 Dallas, Texas 75265

Phone: (972) 917-5653 Fax: (972) 917-4418

or (972) 917-4417

Jacqueline J. Garner Attorney for Applicant Reg. No. 36,682



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Memory With 6T Small Aspect Ratio Cells Having Metal_1 Elements

Physically Connected to Metal_0 Elements

ABSTRACT OF THE DISCLOSURE

A method of forming a memory circuit comprising six transistor memory cells. The memory cells comprise first and second inverters. The inverters comprise respective first and second drive transistors and first and second pull-up transistors. The method also forms a plurality of conducting plugs. A first conducting plug is coupled to the first inverter and a second conducting plug is coupled to the first pull-up transistor and to the gates of the second drive transistor and the second pull-up transistor. A third conducting plug is coupled to the second inverter and a fourth conducting plug coupled to the second pull-up transistor and to the gates of the first drive transistor and the first pull-up transistor. The method also forms conducting elements. A first conducting element contacts the first conducting plug and the second conducting plug and a second conducting element contacts the third conducting plug and the fourth conducting plug.



23494

United States Patent and Trade

COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE

Washington, D.C. 20231

www.uspto.gov

APPLICATION NUMBER

FILING/RECEIPT DATE

FIRST NAMED APPLICANT

ATTORNEY DOCKET NUMBER

09/921,394

P O BOX 655474, M/S 3999

DALLAS, TX 75265

TEXAS INSTRUMENTS INCORPORATED

08/02/2001

Sudhir K. Madan

TI-30841

CONFIRMATION NO. 6004

FORMALITIES LETTER

OC000000006787415

Date Mailed: 09/25/2001

NOTICE TO FILE CORRECTED APPLICATION PAPERS

Filing Date Granted

This application has been accorded an Application Number and Filing Date. The application, however, is informal since it does not comply with the regulations for the reason(s) indicated below. Applicant is given **TWO MONTHS** from the date of this Notice within which to correct the informalities indicated below. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a)

The required item(s) identified below must be timely submitted to avoid abandonment:

An Abstract not to exceed 150 words in length, commencing on a separate sheet (37 CFR 1.72(b)).

A copy of this notice <u>MUST</u> be returned with the reply.

Customer Service Center

Initial Patent Examination Division (703) 308-1202

PART 2 - COPY TO BE RETURNED WITH RESPONSE